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Intel Legal Team

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Page 1 of 52

Urgent

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Date: October 28, 2004

To:
Examiner:
Nema O. Berezny
USPTO

Fax:
(703) 872-9306

Art Unit:
2813

From:
Jay P. Beale

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M/S:

Subject: A Process of Vertically Stacking Multiple Wafers Supporting Different...
Application No.: 10/066,645; Inventor: Kellar et. al
Filed: 2/6/2002 Docket No. 042390.P12753

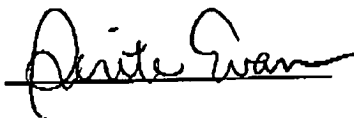
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Serita Evans

Date: 10/28/04



Included in this transmission:

Fax Cover Sheet (1 page)
RCE Transmittal - PTO/SB30 (1 page submitted in duplicate)
Preliminary Amendment (3 pages)
Figures 1A-B and 2 (1 page)
Information Disclosure Statement w/ PTO-1449 (6 pages)
One non-US reference (15 pages)
One non-published application (24 pages)

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No. : 10/066,645
Applicant : Kellar, et al.
Filed : February 6, 2002
TC/AU : 2813
Examiner : Berezny, Nema O.

Confirmation No.: 4506

Docket No. : 42390.P12753
Customer No. : 08791

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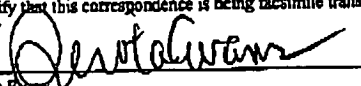
Preliminary Amendment

Examiner,

This Preliminary Amendment accompanies the filing of a Request for Continuing Examination in this application.

Amendments to the Specification begin on page 2 of this paper.

Remarks begin on page 3 of this paper.

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